

Notice of References CitedApplication/Control No.
09/651,229Applicant(s)/Patent Under
Reexamination
WARD, WILLIAM P.Examiner
Pierre-Michel BatailleArt Unit
2186

Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,393,534	05-2002	Chen et al.	711/158
	B	US-6,381,658	04-2002	Swaminathan, Muthukumar P.	710/52
	C	US-6,076,139	06-2000	Welker et al.	711/104
	D	US-6,026,464	02-2000	Cohen, Gary Leon	710/241
	E	US-5,894,586	04-1999	Marks et al.	710/22
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Zacharenia Xanthaki "A Memory Controller For Interleaving Over A Single Rambus" www.ics.forth.gr/proj/arch-vlsi/sw_arch/interlRambus.ps.gz, Feb, 1994.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.